WHAT IS CLAIMED IS:

1. A plasma display, comprising:

first and second substrates opposing one another;

a plurality of first electrodes formed on a surface of the first substrate facing the second substrate;

a first dielectric layer covering the first electrodes;

a plurality of main barrier ribs integrally formed on a surface of the second substrate facing the first substrate, the main barrier ribs defining a plurality of discharge cells;

a plurality of electrode barrier ribs formed on the second substrate between the main barrier ribs;

a second electrode and a second dielectric layer being formed on a distal end of each of the electrode barrier ribs;

phosphor layers formed within the discharge cells; and

discharge gas provided in the discharge cells.

2. The plasma display of claim 1, with the second dielectric layer being formed on the second electrode formed on the distal end of each of the electrode barrier ribs.

3. The plasma display of claim 1, further comprising a third dielectric layer being formed on a distal end of each of the main barrier ribs, and a height of an upper surface of the third dielectric

layer and a height of an upper surface of the second dielectric layer being substantially the same.

4. The plasma display of claim 1, further comprising a third dielectric layer being formed on a distal end of each of the main barrier ribs, and a height of an upper surface of the third dielectric layer being greater than a height of an upper surface of the second dielectric layer.

5. The plasma display of claim 1, wherein one of the second electrodes is formed on a distal end of each of the main barrier ribs and the electrode barrier ribs.

6. The plasma display of claim 1, wherein one of the second electrodes is formed on a distal end of each of the electrode barrier ribs.

7. The plasma display of claim 1, wherein the electrode barrier ribs are formed integrally with the second substrate.

8. The plasma display of claim 1 wherein each discharge cell is divided into a plurality of partitioned discharge cells in which the same phosphor layer is formed.

9. The plasma display of claim 8, wherein each discharge cell is divided into two partitioned discharge cells.

10. The plasma display of claim 8, wherein the partitioned discharge cells include concave surfaces, and a width of each of the partitioned discharge cells are formed to correspond to a color displayed by the particular partitioned discharge cell.

11. The plasma display of claim 10, wherein the partitioned discharge cells displaying blue include a larger width than the partitioned discharge cells displaying green, and the partitioned discharge cells displaying green have a larger width than the partitioned discharge cells displaying red.

12. A method for manufacturing a plasma display, comprising:

integrally forming a plurality of main barrier ribs on a plasma display substrate, the main barrier ribs defining a plurality of discharge cells;

forming electrode barrier ribs between the main barrier ribs;

forming an electrode on a distal end of each of the electrode barrier ribs; and

forming a dielectric layer on each of the electrodes.

13. The method of claim 12, wherein the main barrier ribs and the electrode barrier ribs are formed simultaneously.

14. The method of claim 12, wherein the main barrier ribs, the electrode barrier ribs, and the electrodes are formed simultaneously.

15. The method of claim 12, wherein the main barrier ribs, the electrode barrier ribs, the electrodes, and the dielectric layers are formed simultaneously.

16. The method of claim 12, with the main barrier ribs and electrode barrier ribs being formed by using the second electrodes as a mask.

17. The method of claim 12, with the second electrode forming before the main barrier ribs.

18. The method of claim 12, with the main barrier ribs being integrally formed to the second substrate before the formation of the second electrode and second dielectric layer.

19. A plasma display, comprising:

a first substrate;

a second substrate opposing the first substrate;

a plurality of first electrodes formed on a surface of the first substrate facing the second substrate;

a first dielectric layer covering the first electrodes;

a plurality of main lattice walls integrally formed on a surface of the second substrate facing the first substrate, the main lattice walls defining a plurality of discharge cells;

a plurality of electrode lattice walls integrally formed on the second substrate between the

main lattice walls, each electrode lattice walls dividing each discharge cell formed between the main lattice walls into a plurality of partitioned discharge cells, the partitioned discharge cells for each of the discharged cells accommodating a phosphor layer of the same color;

a second electrode formed on a distal end of each of the electrode lattice walls; and

a second dielectric layer formed on the second electrode formed on the distal end of each of the electrode lattice walls.

20. The plasma display of claim 19, further comprising a third dielectric layer being formed on a distal end of each of the main lattice walls, and a height of an upper surface of the third dielectric layer and a height of an upper surface of the second dielectric layer being substantially the same.

21. The plasma display of claim 19, further comprising a third dielectric layer being formed on a distal end of each of the main lattice walls, and a height of an upper surface of the third dielectric layer being greater than a height of an upper surface of the second dielectric layer.